		Application/Control No.	Applicant(s)/Patent Under Reexamination
Notice of References Cited		10/084,630	LI ET AL.
		Examiner	Art Unit	
		Shahid Al Alam	2162	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-4,611,094	09-1986	Asmuth et al.	379/201.03
*	B	US-5,941,957	08-1999	Ingrassia et al.	709/248
*	C	US-5,951,643	09-1999	Shelton et al.	709/227
*	D	US-5,951,652	09-1999	Ingrassia et al.	709/248
*	E	US-5,954,798	09-1999	Shelton et al.	709/224
*	F	US-6,035,332	03-2000	Ingrassia et al.	709/224
*	G	US-6,490,575	12-2002	Berstis, Viktors	707/3
*	H	US-6,108,703	08-2000	Leighton et al.	709/226
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Iyengar, Arun "Design and Performance of a General-Purpose Software Cache", 1999 IEEE, Pages 329 - 336.
	V	Min, Sang Lyul et al., "Design and Analysis of a Scalable Cache Coherence Scheme Based on Clocks and Timestamps", IEEE Transaction on Parallel and Distributed Systems, Vol. 3, NO. 1, January 1992, Pages 25 - 44.
	W	Jing, Jin et al., "Bit-Sequences: An Adaptive Cache Invalidation Method in Mobile Client/Server Environments", Mobile Networks and Applications, Volume 2, Issue 2, October 1997, Pages 1-23.
	X	Degenaro, Louis et al., "A Middleware System Which Intelligently Caches Query Results", ACM International Conference on Distributed Systems Platforms, New York, April 4 - 7, 2000, Pages 1-22.

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.